Graphin to announce Graphic Capture Board with 2.5Gbps x 4-Lane, GPLAB-2500-4PDPS compliant to D-PHY (version 1.2)

TOKYO, Japan　December 1, 2015

Graphin Co. Ltd. (President, Tomoaki KUROSAWA; Locations, 20-5,Minami-ooi 3-chome, Shinagawa-ku, Tokyo 1400013 JAPAN), a wholly subsidiary of TECHNO HORIZON HOLDINGS CO., LTD. (JASDAQ 6629), has announced that the GPLAB-2500-4PDPS, the company's Image Capture Board developed for evaluation and manufacturing of high-resolution and high-speed CMOS Image Sensor (CIS), is now available. It is fully compliant with the D-PHY (version 1.2) CSI-2 standard for the next generation of smartphones and mobile devices.

GPLAB-2500-4PDPS compliant to D-PHY (version 1.2) CSI-2 is Image Capture Board developed to support high bandwidth of 2.5Gbps x 4 Lane. With using fiber optic interface PC board, output signals from the next generation of smartphones and mobile devices equipped with a high-resolution and high-speed camera modules, can be captured without dropping of any frames. Built-in Power Supply and Power-Consumption-Measurement function allow to execute the evaluation and experiment of manufacturing the next generation of camera modules. GPLAB-2500-4UDPS supportive to USB3.0 is also available to intend simple- and easy-to-use with note-book PC and tablet devices.

Recent emerging smartphones are supported with not only digital-cameras but also 4K movie functions. With our strategic insight focusing on high-speed of image sensors for future generations, our GPLAB-2500-4PDPS and GPLAB-2500-4UDPS are available today ahead of other providers.

About Features of GPLAB-2500-4PDPS/GPLAB-2500-4UDPS:

- Fully compliant to D-PHY 2.5Gbps x 4 lane for high speed image data communications.
- Built-in Power Supply and Power-Consumption-Measurement function developed for the next CMOS Image Sensors.
- GPLAB-2500-4PDPS embedded with fiber optic interfaces which enables high speed PC data transfers without dropping of frames
- GPLAB-2500-4UDPS compliant to USB3.0 which enables simple experimental arrangement

About MIPI:

MIPI Alliance (MIPI) develops interface specifications for mobile and mobile-influenced industries. Founded in 2003, the organization has more than 275 member companies worldwide, more than 15 active working groups, and has delivered more than 45 specifications within the mobile ecosystem in the last decade. Members of the organization include handset manufacturers, device OEMs, software providers, semiconductor companies, application processor developers, IP tool providers, test and test equipment companies, as well as camera, tablet and laptop manufacturers. For more information, please visit www.mipi.org.

About D-PHY:

MIPI's D-PHY is the technology for the off-chip Physical Layer. This PHY allows inter-chip communication for display and camera in mobile applications.

About CSI-2:

CSI-2 known as Camera Serial Interface-2 provides the mobile industry a standard, robust, scalable, low-power, high-speed, cost-effective interface that supports a wide range of imaging solutions for mobile devices.

About Graphin Co.:

Graphin Co., Ltd., a wholly subsidiary of TECHNO HORIZON HOLDINGS CO., LTD. (JASDAQ:6629) was incorporated in January 1993 in an aim to provide products that combine two different domains; the image processing technology of graphics and the information communication technology of interfaces. Graphin has interests in domain of graphics and interface, many hardware products, OEM boards, and semiconductor intellectual properties (SIP) in Japan and has vast experience in the sales and support of various hardware and software. Their strengths include digital consumers, games, data compression algorithm, hardware design capability, FPGA and ASIC design, image processing algorithms, and knowledge of communication protocols. Graphin is a member of the MIPI Alliance. For more information, please visit Graphin at www.g-in.co.jp.

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